

# ACKNOWLEDGEMENT RECEIPT

Electronic Version

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## Title of Invention

PIPELINE-BASED CIRCUIT WITH A POSTPONED  
CLOCK-GATING MECHANISM FOR REDUCING POWER  
CONSUMPTION AND RELATED DRIVING METHOD THEREOF

Submission Type : Utility Patent Filing

Application Number:

10/707455



EFS ID:

52325

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
ICON1	1454

First Named Applicant: Chung-Hui Chen

Attorney Docket Number: FTCP0017USA

Timestamp: 2003-12-16 00:32:47 EDT

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-power-of-attorney-grant	FTCP0017-uspoat.xml	1034	2003-12-16
us-power-of-attorney-grant	us-power-of-attorney-grant.dtd	4923	2003-12-16
us-power-of-attorney-grant	us-power-of-attorney-grant.xsl	12448	2003-12-16
us-assignment	FTCP0017-usasn.xml	1490	2003-12-16
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us-assignment	us-assignment.dtd	10898	2003-12-16
us-assignment	FTCP0017ASS1.tif	59824	2003-12-16
us-request	FTCP0017-usrequ.xml	1468	2003-12-16
us-request	us-request.dtd	19064	2003-12-16
us-request	us-request.xsl	33300	2003-12-16
us-fee-sheet	FTCP0017-usfees.xml	2064	2003-12-16
us-fee-sheet	us-fee-sheet.xsl	24912	2003-12-16
us-fee-sheet	us-fee-sheet.dtd	11069	2003-12-16
us-declaration	FTCP0017-usdecl.xml	701	2003-12-16
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application-body	FTCP0017-trans.xml	48943	2003-12-16
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application-body	wipo.ent	4956	2003-12-16
application-body	mathml2.dtd	54588	2003-12-16
application-body	mathml2-qname-1.mod	13225	2003-12-16
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application-body	isoamsb.ent	3988	2003-12-16
application-body	isoamsc.ent	1460	2003-12-16
application-body	isoamsn.ent	5620	2003-12-16
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application-body	Ftcp0017usa-04.tif	3638	2003-12-16
application-body	Ftcp0017usa-05.tif	3858	2003-12-16
application-body	Ftcp0017usa-06.tif	28584	2003-12-16
us-declaration	FTCP0017DEC1.tif	43384	2003-12-16
us-declaration	FTCP0017DEC2.tif	19474	2003-12-16
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